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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of
Toshiro HIRAMOTO et al.
Application No.: 09/389,321

ATTN: DRAFTING BRANCH
Allowed: September 20, 2005
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Title: MOS TRANSISTOR WITH A CONTROLLED THRESHOLD VOLTAGE

SUBMISSION OF DRAWINGS

MAIL STOP ISSUE FEE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Enclosed please find 9 sheet(s) of drawings for review by the Patent and Trademark Office.

Should the enclosed drawing(s) require changes, it is respectfully requested that the Patent and Trademark Office notify the undersigned of same.

Respectfully submitted,

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